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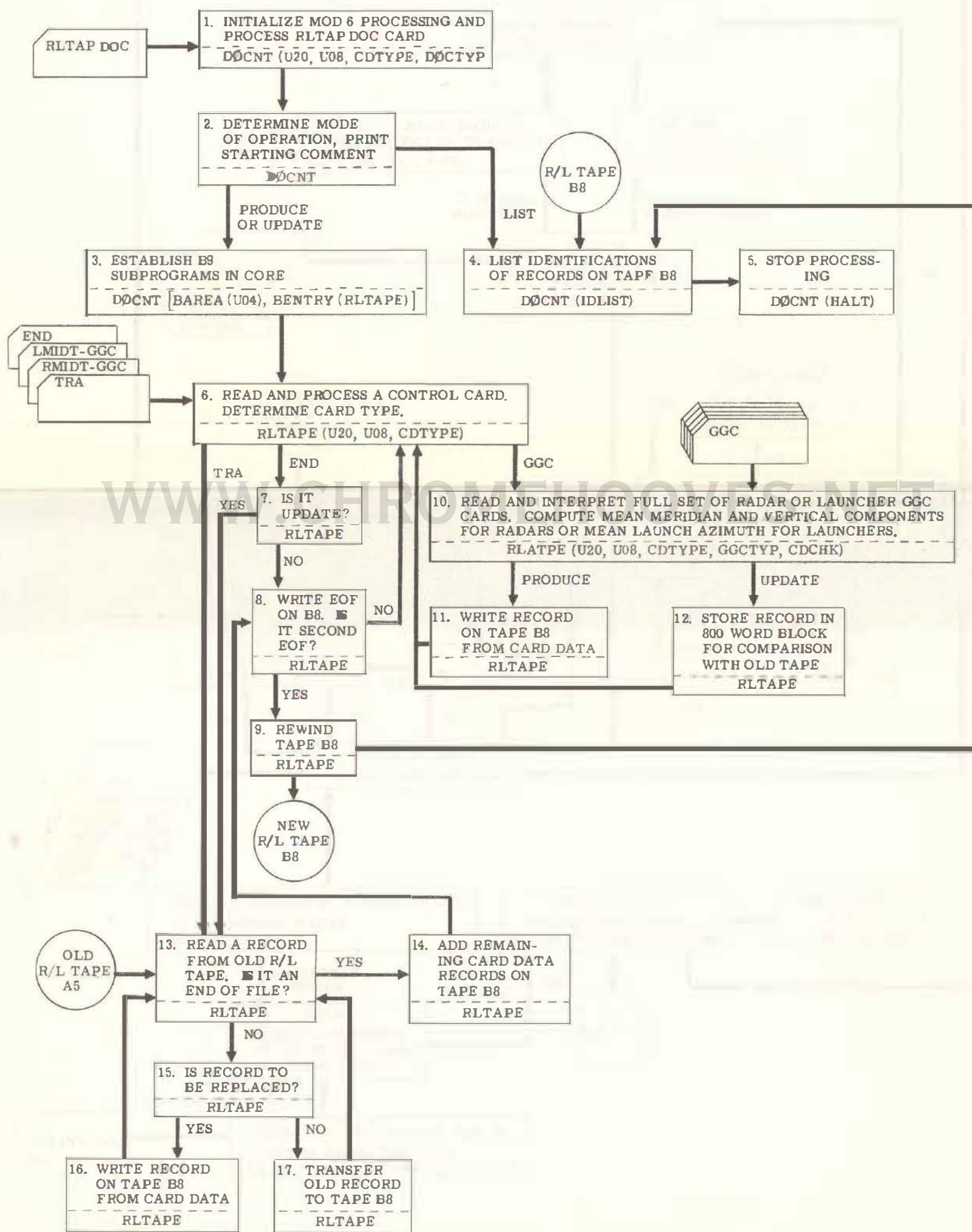


Figure 2-1. Radar-Launcher Tape Generation Flow Diagram

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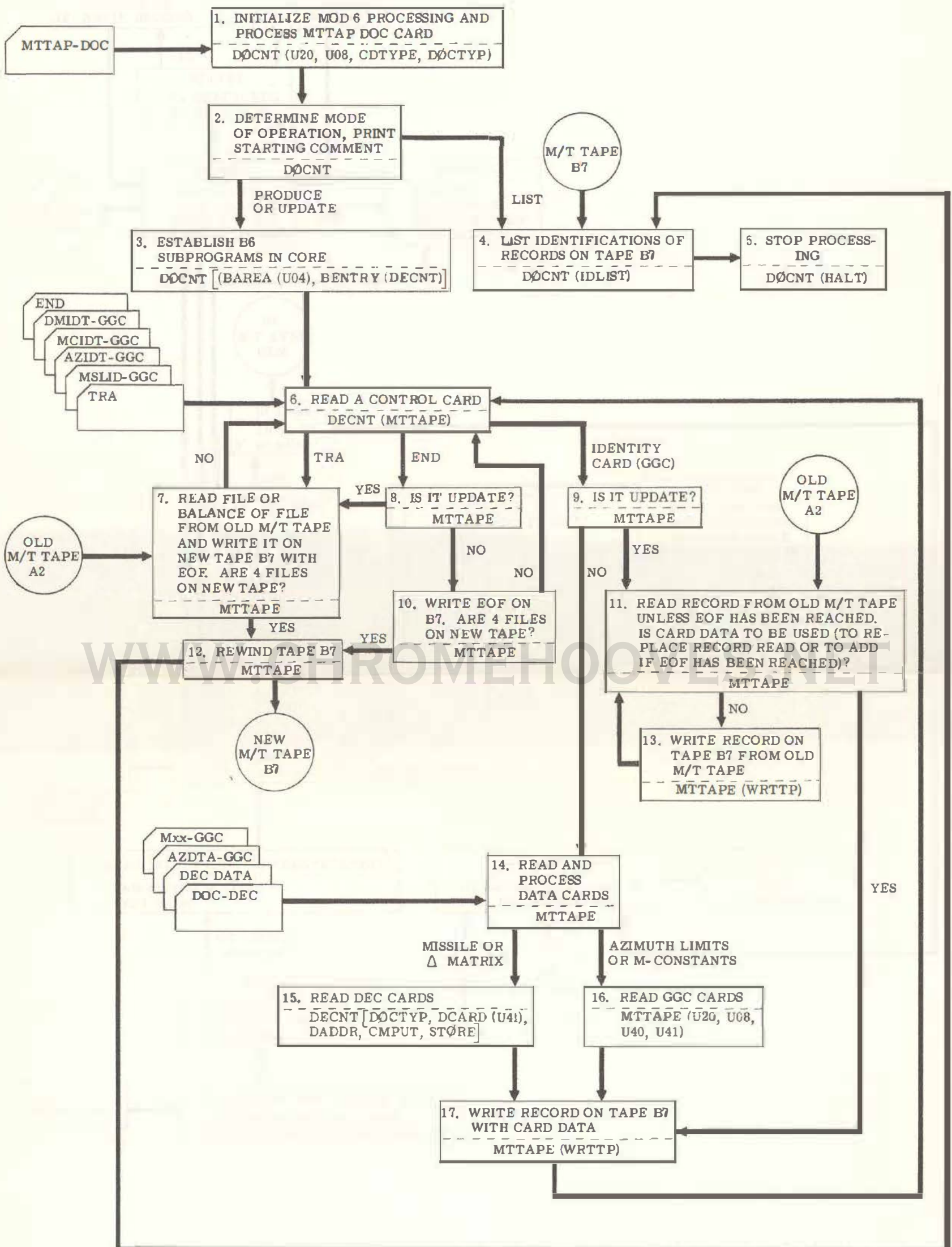
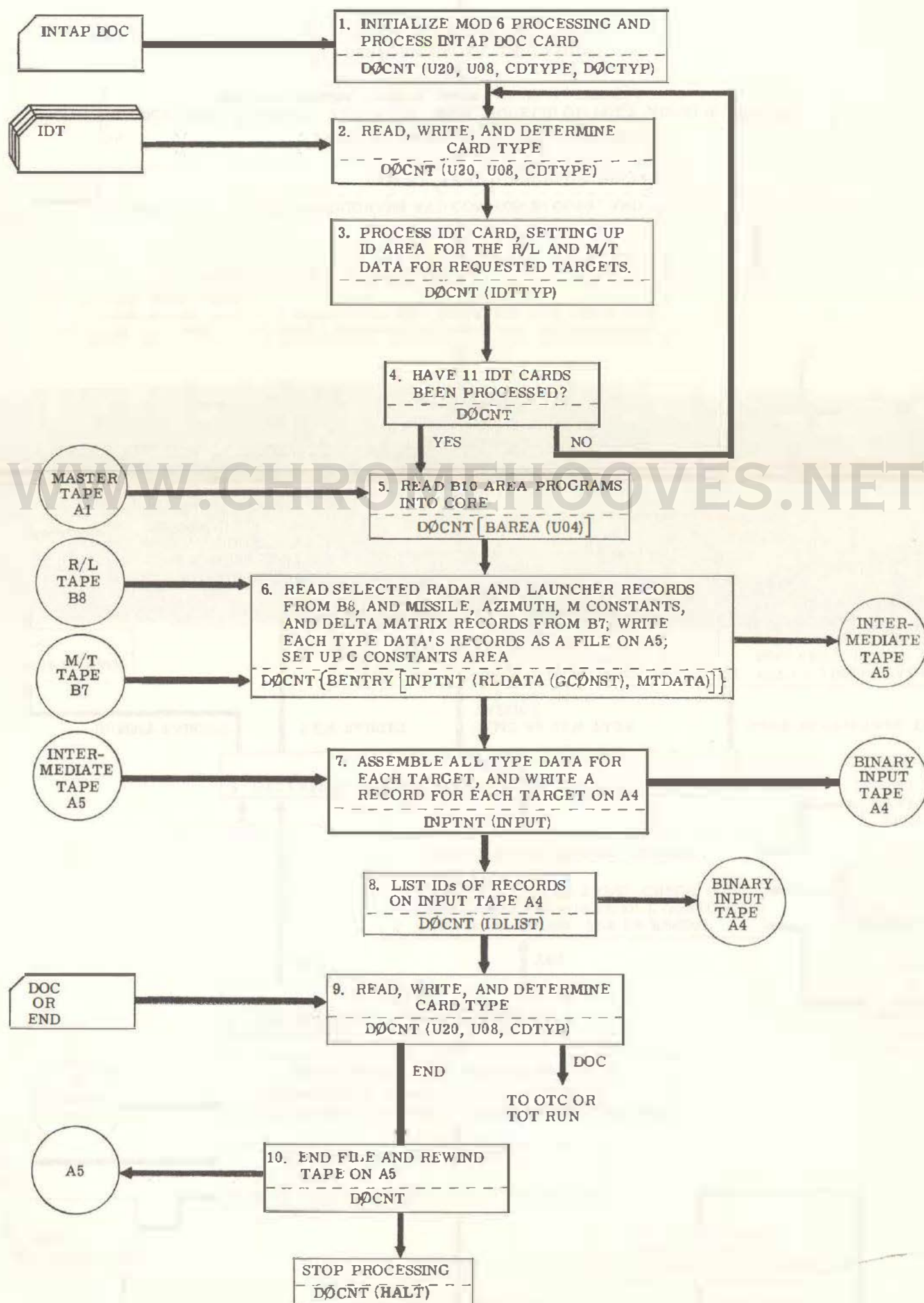


Figure 2-1A. Missile-Trajectory Tape Generation Flow Diagram

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Figure 2-1B. Binary Input Tape Generation Flow Diagram

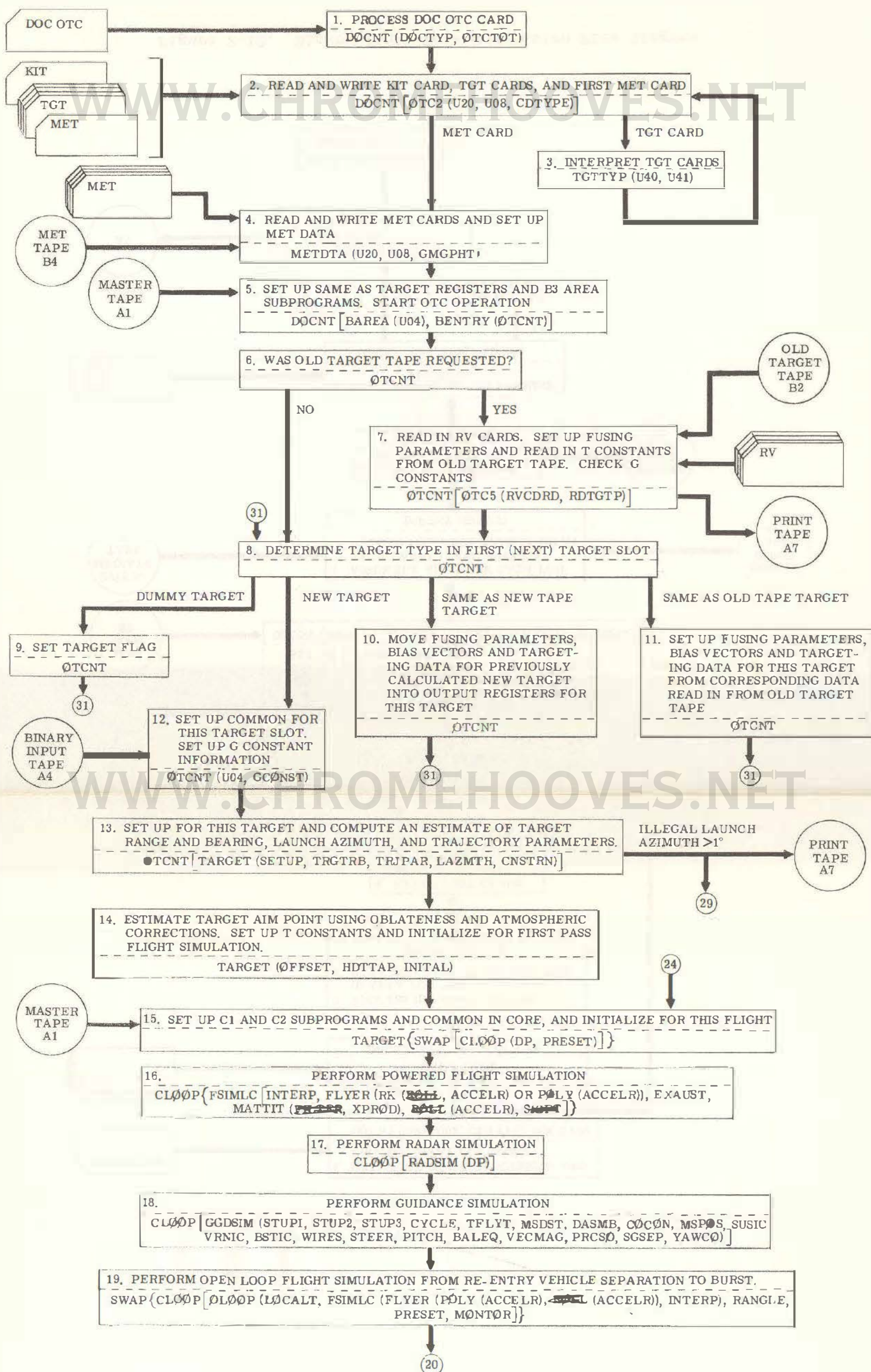


Figure 2-1C. Offset Target Computation Flow Diagram (Sheet 1 of 2)

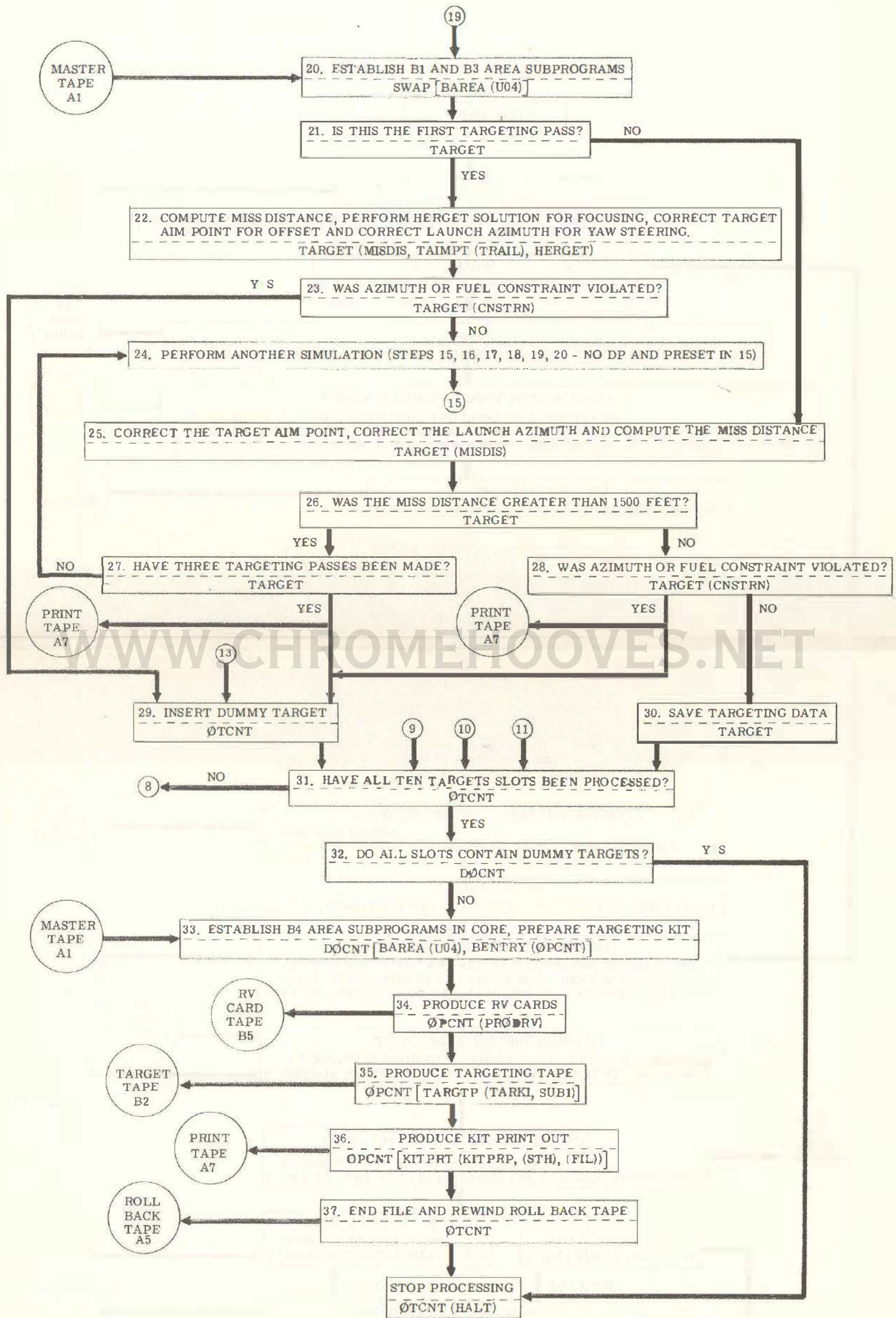
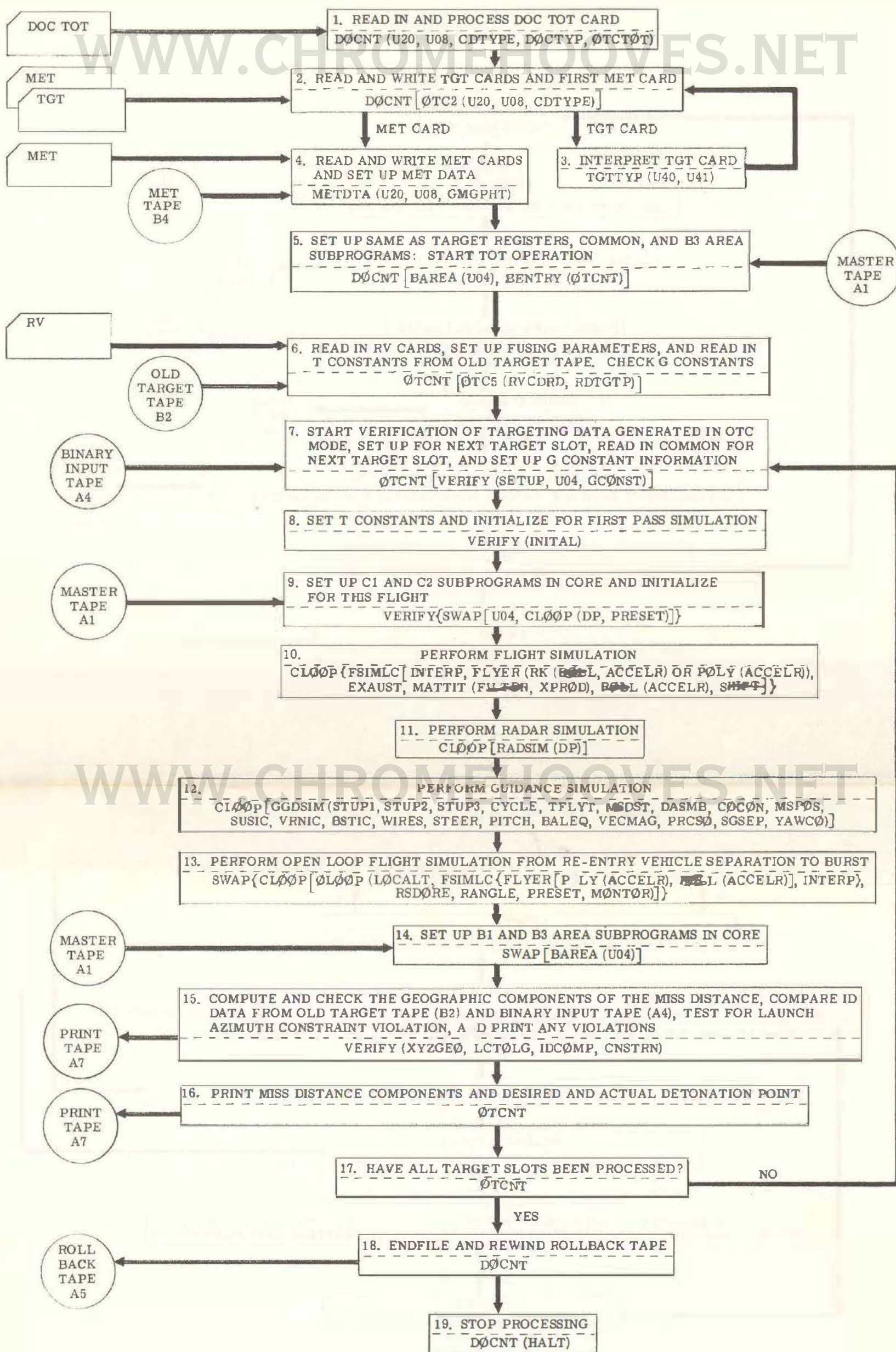


Figure 2-1C. Offset Target Computation Flow Diagram (Sheet 2 of 2)

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Figure 2-1D. Output Data Verification Flow Diagram

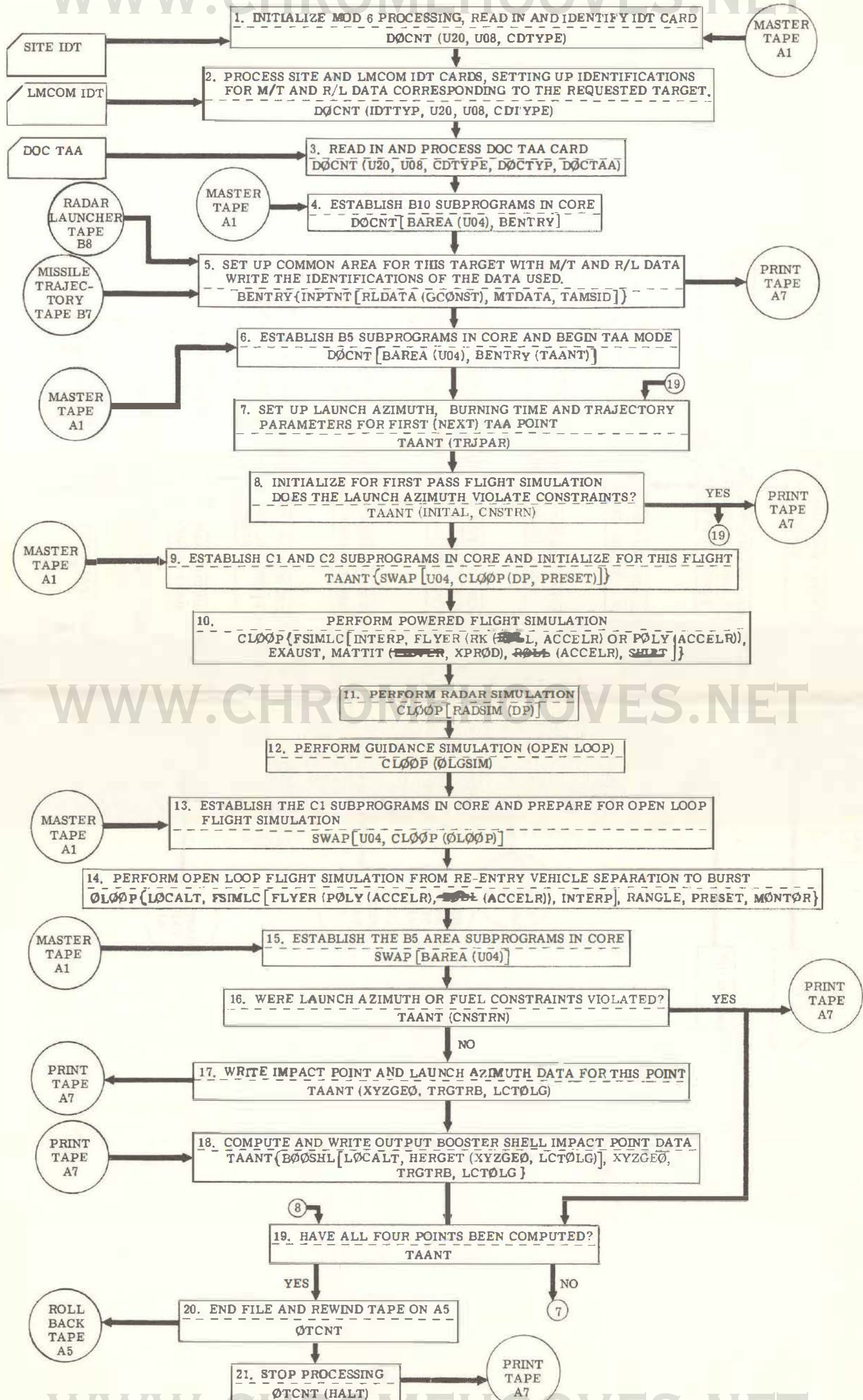


Figure 2-1E. Target Accessibility Area Determination Flow Diagram

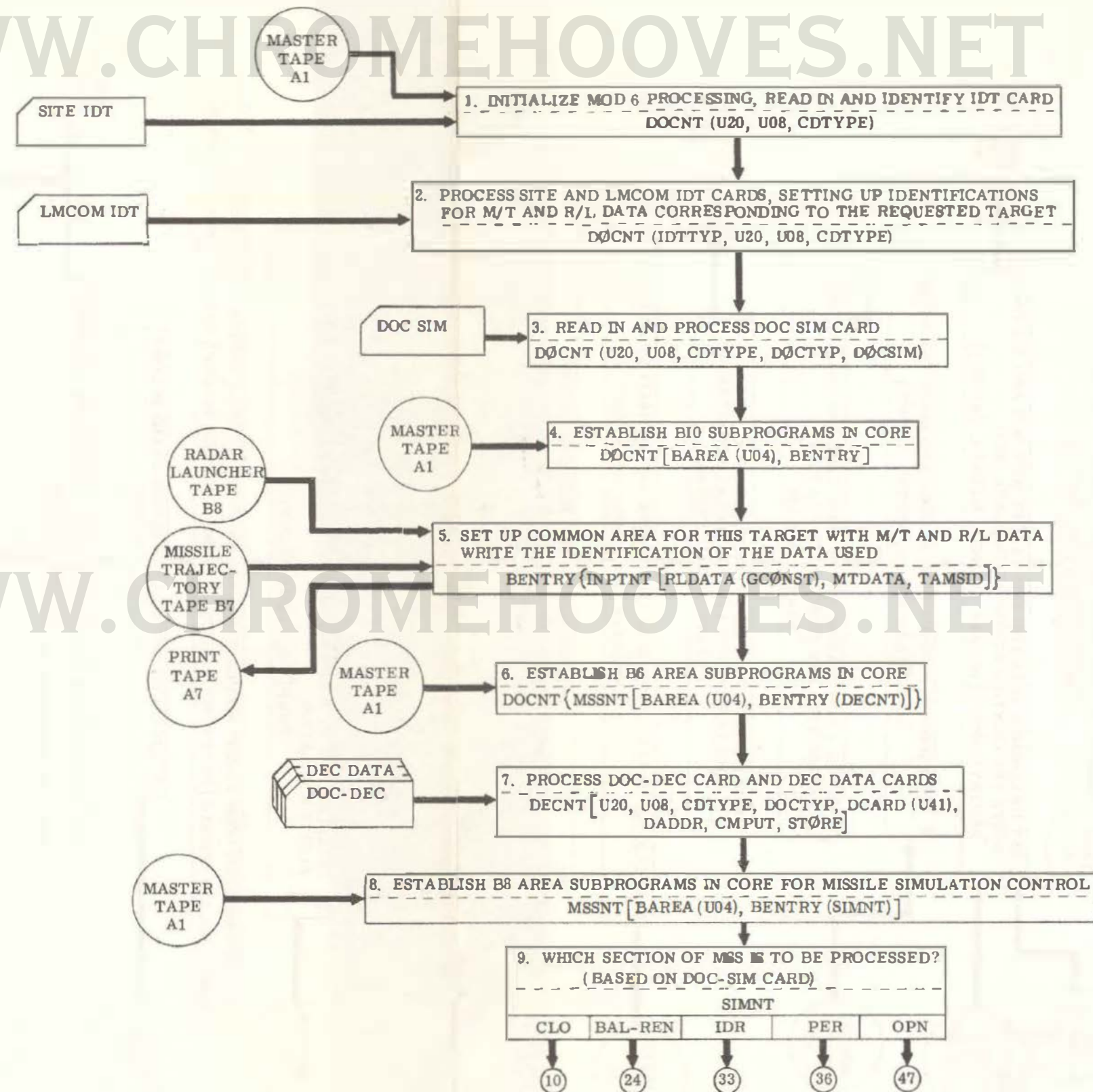


Figure 2-1F. Missile System Simulation and Range Safety Data Flow Diagram (Sheet 1 of 4)

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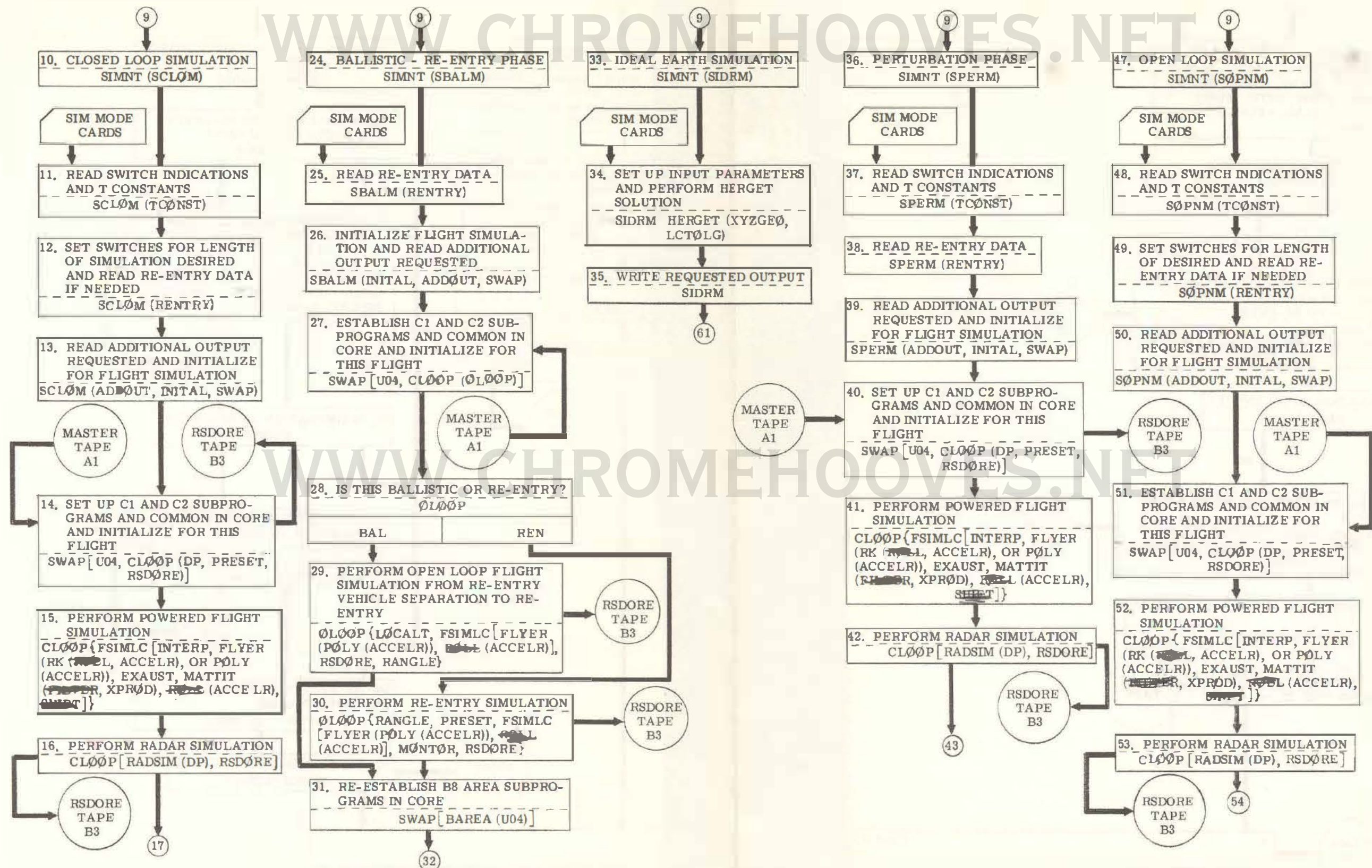


Figure 2-1F. Missile System Simulation and Range Safety Data
Flow Diagram (Sheet 2 of 4)

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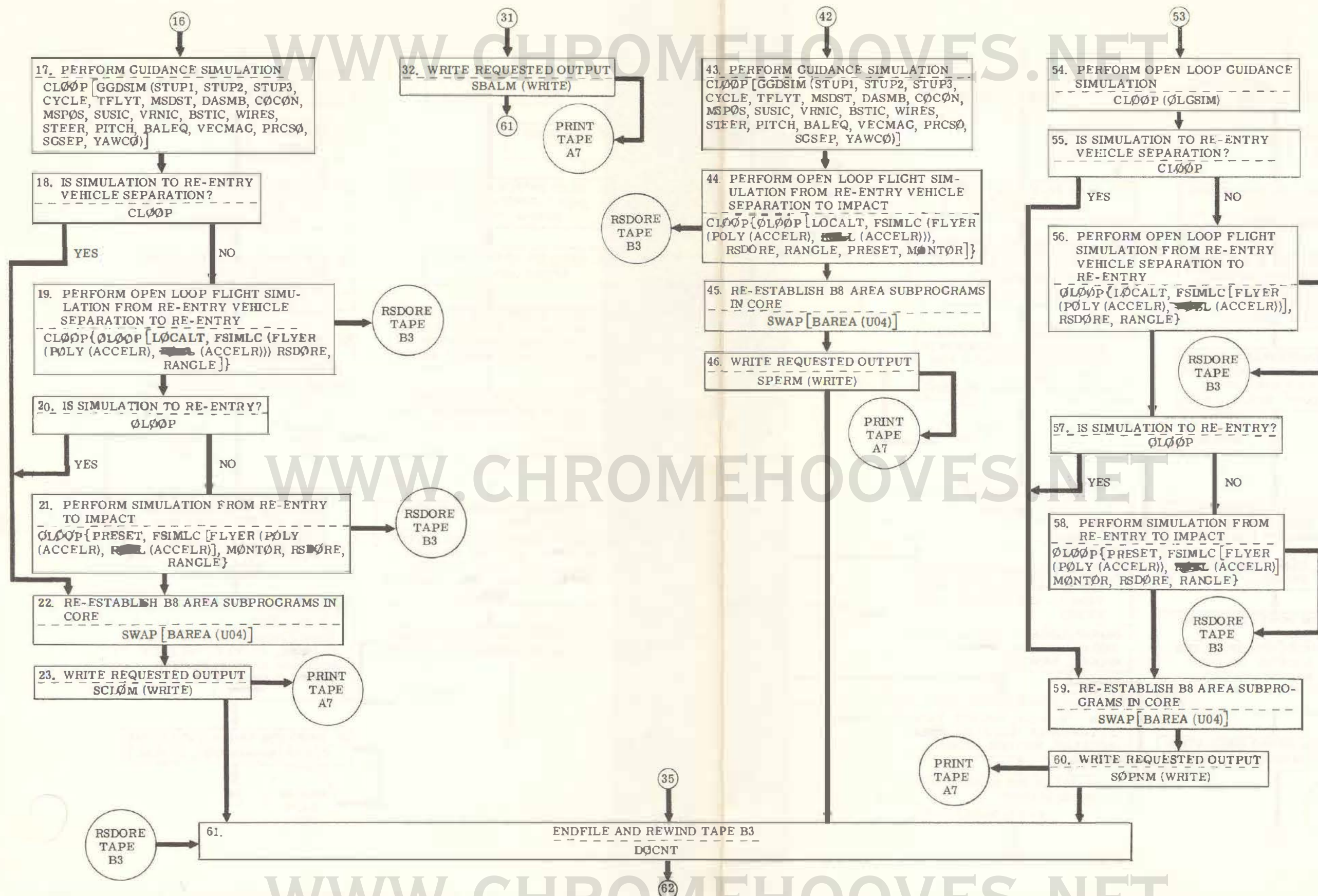


Figure 2-1F. Missile System Simulation and Range Safety
Data Flow Diagram (Sheet 3 of 4)

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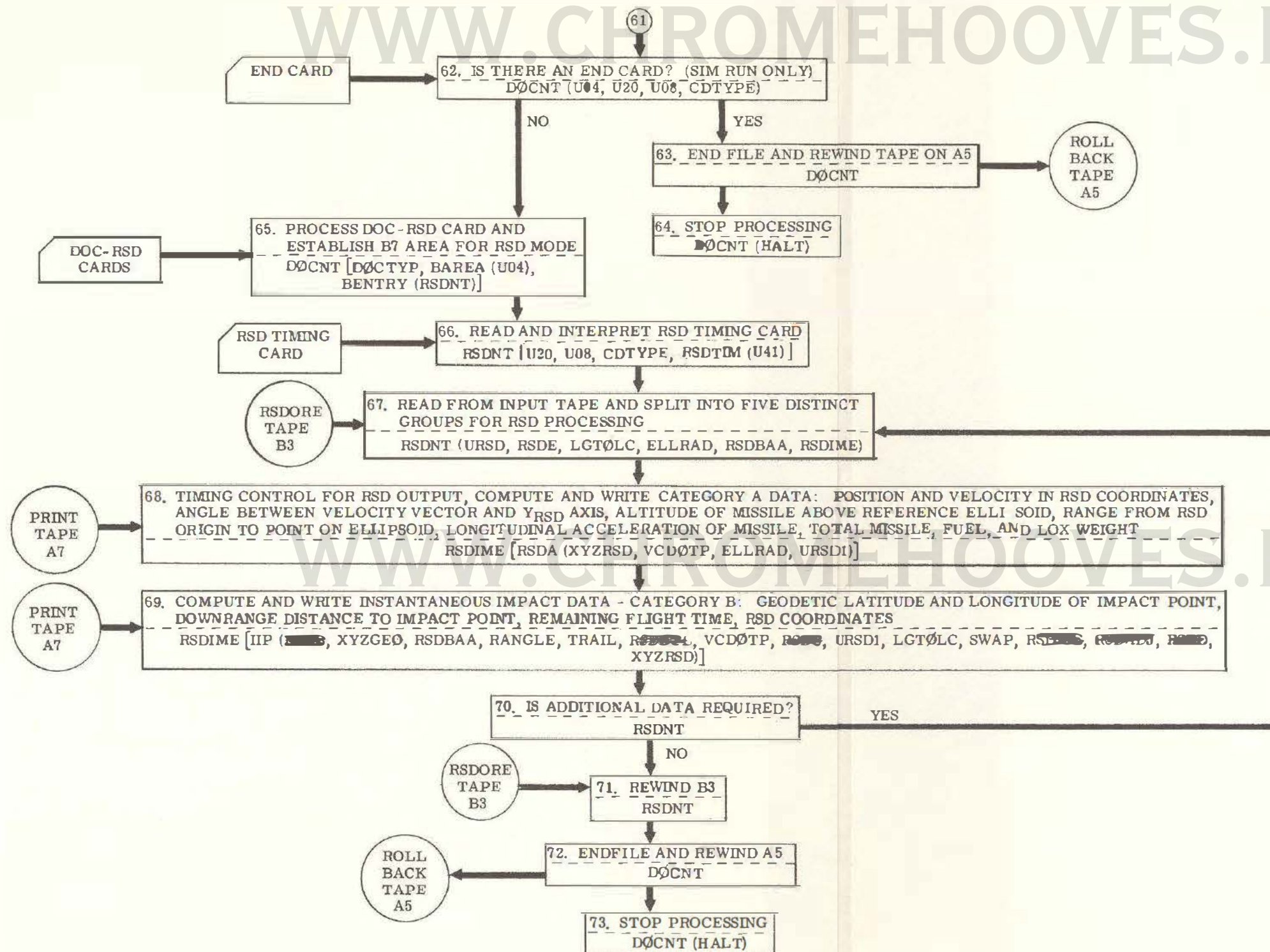


Figure 2-1F. Missile System Simulation and Range Safety Data Flow Diagram (Sheet 4 of 4)

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